CLAIMS

I claim:

1. A computerized method for producing code in an architecture description language, said method comprising the steps of:

a. reading an opcode summary table;

b. analyzing said opcode summary table to determine the layout of said opcode summary table;

c. generating code for an instruction in architecture description language format; and

d. repeating said generating step for each line in said opcode summary table, resulting in an ADL representation of the opcode summary table.

2. The method of claim 1 where the opcode summary table is provided in a spreadsheet.

3. The method of claim 1 where the opcode summary table is provided in a comma separated value format.

4. A computerized method for producing code in an architecture description language format, said method comprising the steps of:

a. reading an opcode summary table;

b. creating a plurality of output files;

c. analyzing said opcode summary table to determine the layout of said opcode summary table;

d. determining the beginning of a group from said opcode summary table;

e. generating root code for the hierarchy in architecture description language format based on said grouping;

f. cycling through each group to generate detailed code in architecture language format;

g. repeating said cycling step until the end of the opcode summary table is reached; and

h. closing said plurality of output files.

5. The method of claim 4 where the opcode summary table is provided in a spreadsheet.

6. The method of claim 4 where the opcode summary table is provided in a comma separated value format.

7. The method of claim 4 where the opcode summary table is pre-formatted such that the opcodes are separated into groups prior to being read.

8. The method of claim 4 where said cycling step further comprises determining the presence of sub-groups within said group and generating detailed code for each sub-group within said group.

9. A computer program comprising:

a first computer code section for reading an opcode summary table having a plurality of entries representative of a like plurality of microprocessor instructions;

a second computer code section for producing a grouping of at least two of said entries in accordance with a grouping criteria; and

a third computer code section for generating an encoded representation of said grouping.